

SPURIOUS TIMEOUT DETECTION IN TCP BASED NETWORKS
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Attorney Docket 223817, Telephone (312) 616-5600

FIG. 1

```

graph TD
    PS["Power Source  
(e.g., Battery/Fuel cell)"] --- 112[112]
    112 --- CC["Communication Connection(s)"]
    CC --- 114[114]
    114 --- ID["Input Device(s)"]
    ID --- 116[116]
    116 --- OD["Output Device(s)"]
    OD --- 110[110]
    110 --- NRS["Non-Removable Storage"]
    NRS --- 108[108]
    108 --- RS["Removable Storage"]
    RS --- 106[106]
    106 --- 100[100]
    100 --- 102[102]
    102 --- PU["Processing Unit"]
    PU --- SM["System Memory"]
    SM --- 104[104]
    104 --- Vol["Volatile"]
    Vol --- NV["Non-Volatile"]
    NV --- 106
  
```

The diagram illustrates a system architecture with the following components and their connections:

- Power Source (e.g., Battery/Fuel cell)** (112) connects to **Communication Connection(s)** (114).
- Communication Connection(s)** (114) connects to **Input Device(s)** (116).
- Input Device(s)** (116) connects to **Output Device(s)** (110).
- Output Device(s)** (110) connects to **Non-Removable Storage** (108).
- Non-Removable Storage** (108) connects to **Removable Storage** (106).
- Removable Storage** (106) connects to **System Memory** (100).
- System Memory** (100) connects to **Processing Unit** (102).
- Processing Unit** (102) connects to **Volatile** and **Non-Volatile** memory.
- Volatile** and **Non-Volatile** memory both connect to **Communication Connection(s)** (114).

FIG. 2

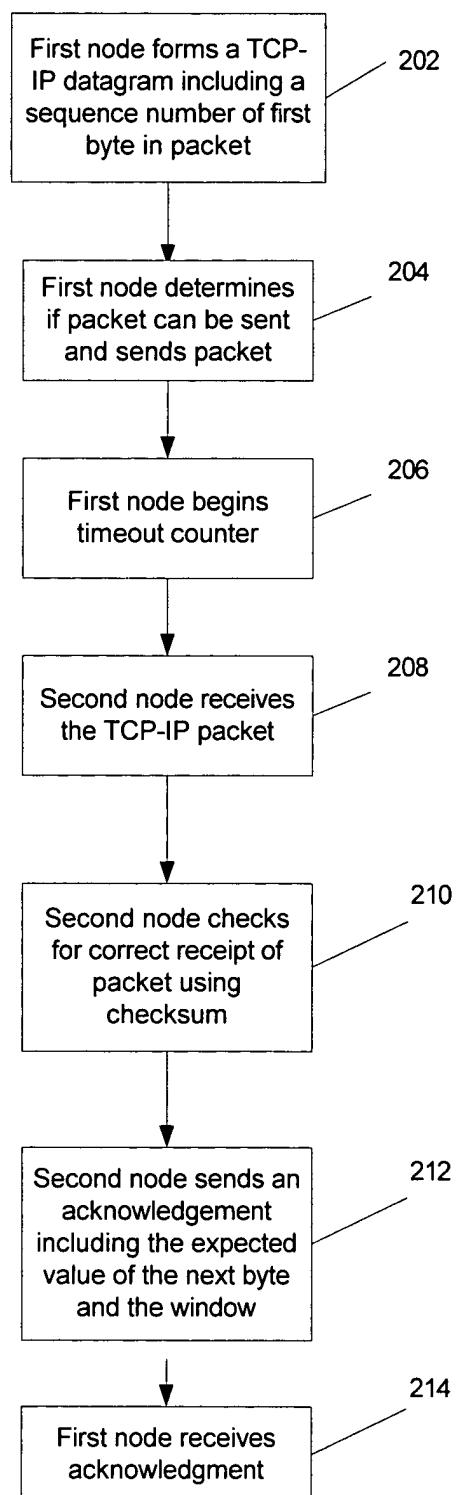
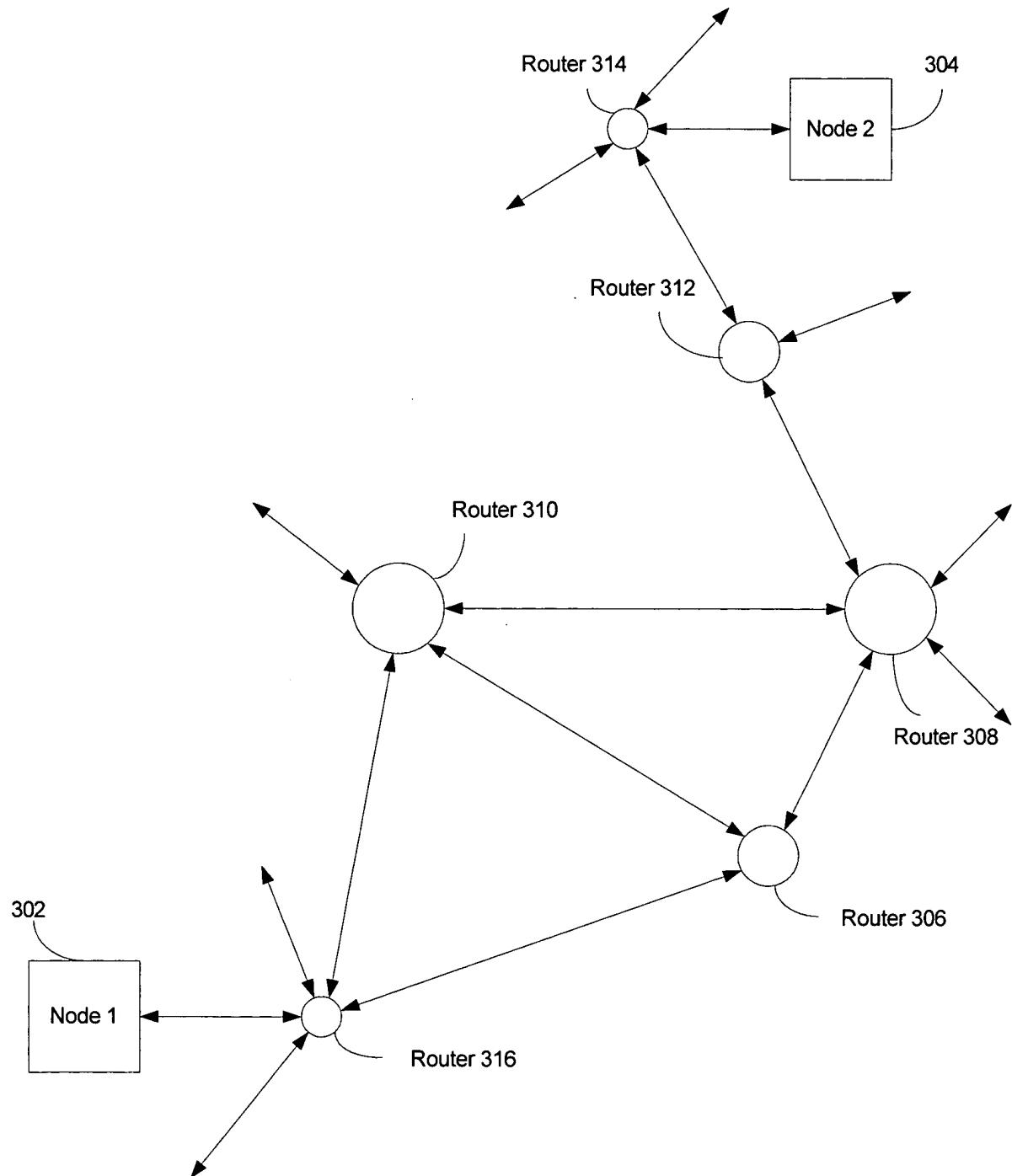


FIG 3



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FIG 4

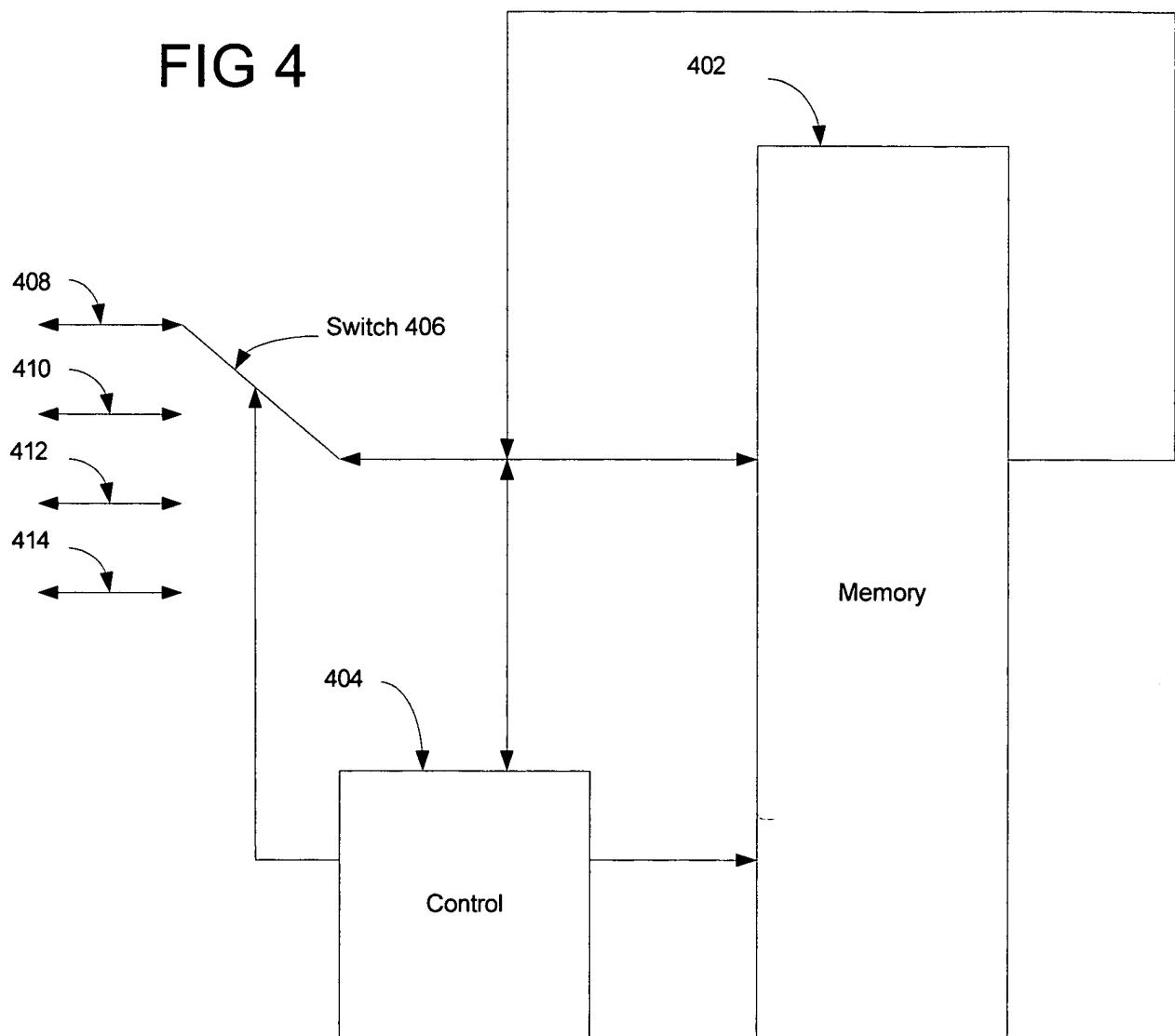
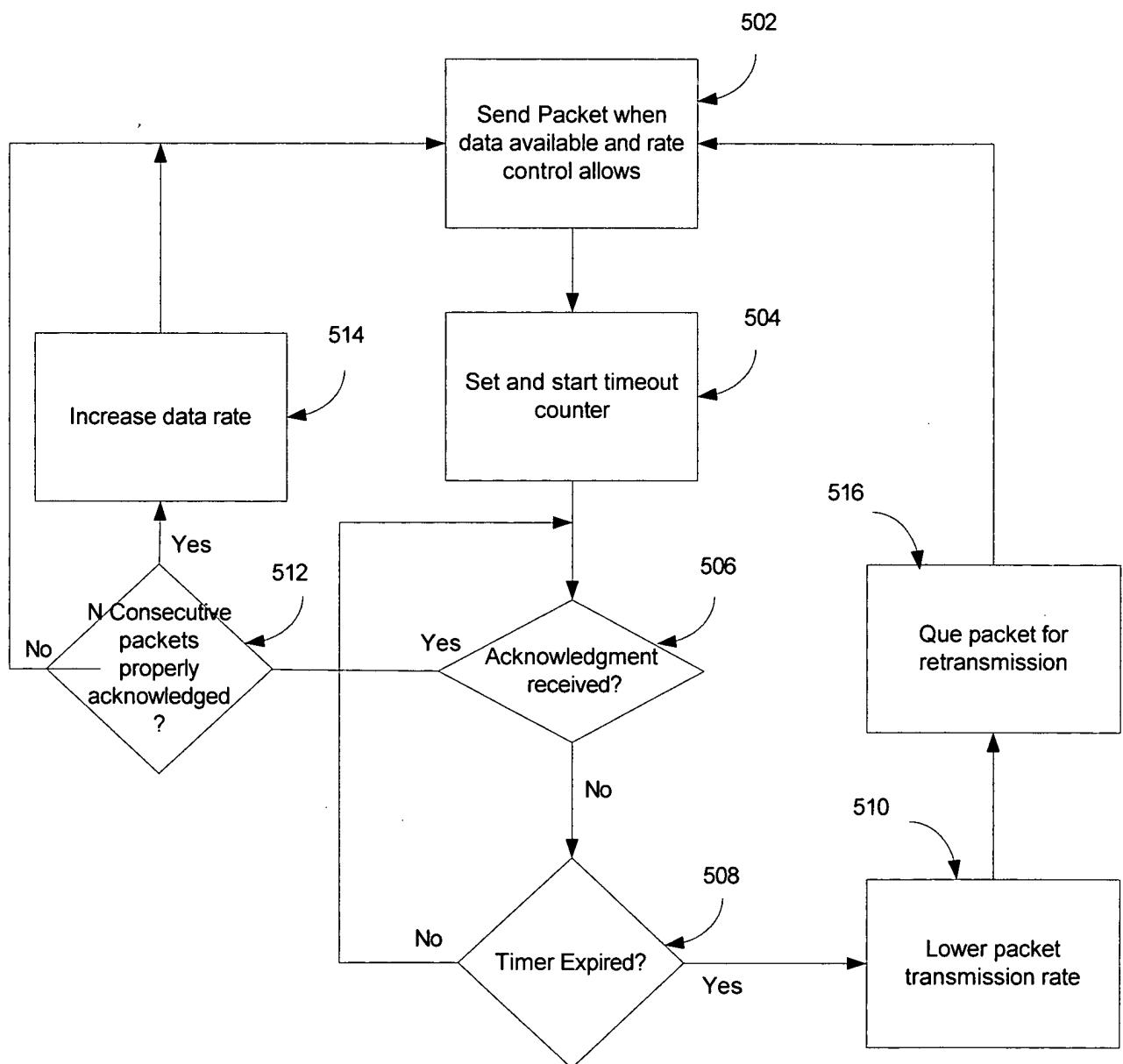


FIG 5



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FIG 6

